

#2/Z.D.S.
5622

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Hiromi Yatsuda GROUP: Unknown

SERIAL NO: Unknown EXAMINER: Unknown

FILED: Herewith

FOR: ELECTRONIC COMPONENT TO BE MOUNTED ON A CIRCUIT
BOARD HAVING ELECTRONIC CIRCUIT DEVICE SEALED
THEREIN AND METHOD OF MANUFACTURING THE SAME

JC821 U.S. PRO
10/033397
12/27/01

Assistant Commissioner of Patents
Washington, D.C. 20231

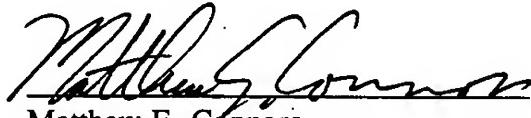
Sir:

INFORMATION DISCLOSURE STATEMENT

In compliance with 37 C.F.R. §§1.56, 1.97, and 1.98, Applicant submits copies of the documents listed on the attached Form PTO-1449.

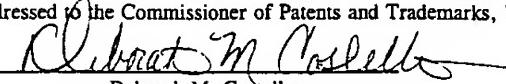
The Commissioner is authorized to charge Deposit Order Account No. 19-0079 for any further fee that is required.

Respectfully submitted,



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I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the Commissioner of Patents and Trademarks, Washington, D.C. 20231.



Deborah M. Costello

5/30/00
Date